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TRANSMITTAL FORM <i>(to be used for all correspondence after initial filing)</i>		Application / Conf. No.	10/619,136 / 6066
		Filing Date	July 14, 2003
		First Named Inventor	David Mark
		Examiner Name	Jimmy Nguyen
		Art Unit	2829
Express Mail Receipt No.		Patent No.	
Total Number of Pages in This Submission		Attorney Docket Number	X-1269-1P US

ENCLOSURES *(check all that apply)*

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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

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Date	September 30, 2004	Charge any additional fees required/credit any overpayment to our Deposit Account Number: 24-0040

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X-1269-1P US
10/619,136

PATENT
Conf. No. 6066

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: David Mark et al.
Assignee: Xilinx, Inc.
Title: "Method for Gross I/O Functional Test at
Wafer Sort"
Serial No.: 10/619,136 Filing Date: 07/14/2003
Examiner: Jimmy Nguyen Art Unit: 2829
Docket No.: X-1269-1P US Conf. No.: 6066

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AMENDMENT IN RESPONSE TO FIRST OFFICE ACTION

In response to the First Office Action mailed from the Patent Office on June 30, 2004, Applicants submit the following amendments and remarks.

Amendments to the Claims begin on page 2 of this paper.

Remarks begin on page 7 of this paper.